Jyotsna Sengupta

Dr. Jyotsna Sengupta is a Professor in Department of Computer Science at Punjabi University, Patiala, Punjab, India. She earned her Ph.D. degree in Computer Science and Engineering from Thapar University, Patiala, Punjab, India, MS degree in Computer Science and Engineering from Santa Clara University, Santa Clara, California, U.S.A., and Bachelor's of Engineering degree in Electronics and Communications from Thapar University, Patiala, Punjab, India. She has published more than 70 research papers in various International and National Conferences and Journals. She has authored two books and edited one book. She is a member of IEEE and the editor of many international journals. Her current research interests include Distributed Systems, Mobile Networks, Cloud Computing and Network Security

Educational qualifications:

- 1. Ph.D. (Computer Science & Engineering.) (Research in Parallel Processing) Thapar Institute of Engg. & Tech., Patiala, India, 2002.
- 2. MS (Computer Science & Engineering.)
 Santa Clara University, Santa Clara, California, U.S.A., 1985
- 3. B.E. (Electronics & Communications)
 Thapar Institute of Engg. & Tech., Patiala, India, 1982.

Experience (29 years)

- 1. Professor, Deptt. Computer Science & Engg., Punjabi University, Patiala 147002, January 1, 2010-continuing
- 2. Reader, Deptt. Computer Science & Engg., Punjabi University, Patiala 147002, July 2002-Dec. 31, 2009
- 3. Senior Lecturer, Deptt. Of Computer Science & Engineering, Punjabi University, Patiala 147002, July 1997- June 2002
- 4. Lecturer, Deptt. Of Computer Science & Engineering, Punjabi University, Patiala, 147002, Feb. 1989-June 1997.
- 5. Lecturer, Deptt. Computer Science, Thapar Institute of Engg. & Tech., Patiala, Feb. April, 1989 (In parallel with Deptt. Of Computer Science, Punjabi University)
- 6. Lecturer, Deptt. Computer Science, Delhi University, Delhi, Feb. 1987-September 1987
- 7. EXL Ltd., Sunnyvale, California, U.S.A., 'Software Engineer', June 1985-June 86
- 8. Research Assistant, Biomedical Department, IIT, Delhi, June-December 1982

Books

- Jyotsna Sengupta, 'Fundamentals of Computer Organization and Architecture',
 NuTech Books, Deep and Deep Publications, New Delhi, 2009, ISBN 978-81-8450-186-5, pages 258.
- 2. Jyotsna Sengupta, 'Interconnection Networks for Parallel Processing', NuTech Books, Deep and Deep Publications, New Delhi, 2005, ISBN 81-7629-736-4, pages 135

Membership to various bodies

- Member IEEE Computer Society (International Society of Electrical and Electronics Engineers)
- Life member Computer Society of India
- Life member Punjab Science Congress